ABSTRACT

A method and system for bidirectional data and power transmission are shown and described. An exemplary embodiment comprises a current receiver, including a microprocessor and a driver, and a plurality of nodes connected to the current receiver through a plurality of wires that allow both power supply and bidirectional data transfer between the current receiver and the plurality of nodes. The use of the plurality of wires for both power and data transmission offers significant advantages over the prior art in terms of weight reduction and system modularity.

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